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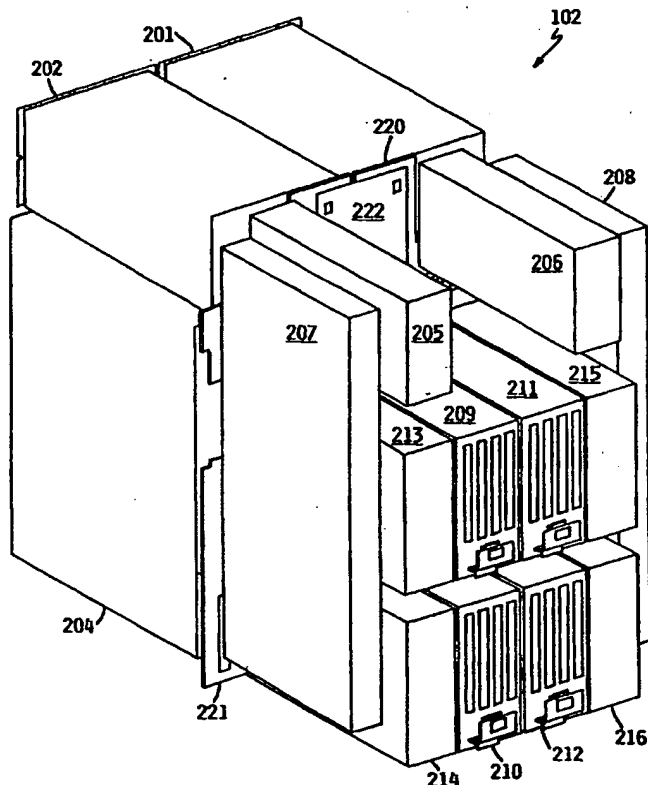
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(54) Title: ELECTRONIC CIRCUIT CARD ASSEMBLY

(57) Abstract

An electronic system contains a backplane circuit card assembly for distribution of electrical signals to one or more modules. The card assembly includes a plurality of pluggably connected base cards (220, 221), which provide redundant function. Preferably, two base cards (220, 221) in a single plane are connected by a single smaller parallel offset jumper card (222), the cards being coupled by pluggable connectors (301, 302, 401, 402, 501-504). The system preferably includes redundant power supply modules (201, 202) and redundant functional modules (203-216), which plug into the backplane from both sides. The modules connected to any one of the base cards provide minimum system functionality in the absence of the other card and its modules. Due to the redundant function, it is not only possible to replace any module, but is possible to replace a base card itself, while the system remains operational. The exemplary embodiment is an intelligent redundant array of independent disks (RAID) storage server having concurrent maintenance capability.



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Description

ELECTRONIC CIRCUIT CARD ASSEMBLY

FIELD OF THE INVENTION

5 The present invention relates to electronic circuit cards, and in particular to "backplane" cards of a type used to distribute power or other signals to other attached modules.

BACKGROUND OF THE INVENTION

10 The spectacular proliferation of electronic devices, particularly computers, in modern society, both in numbers and complexity, demands that such devices satisfy ever increasing standards of reliability and serviceability to avoid degeneration into chaos. In the early days of the computer industry, relatively high failure rates and
15 corresponding "down time" when the computer system was unavailable to perform useful work were accepted as the norm. As the industry has matured, computers have become more reliable, and users have come to depend on these systems being available when needed. This dependence has
20 become so profound that, for many businesses, the mere unavailability of the computer system for any appreciable length of time can cause significant commercial injury.

25 In the early days of the computer industry, a computer component was replaced by shutting off power to the system, replacing the component, and re-powering the system. This is, of course, a logical way to fix a toaster, but the complexity of modern computers makes this undesirable. It is not possible to simply shut off power and turn it back on as one would a light bulb. A computer system's state and
30 data must be saved when it is powered down. Its software must be re-loaded, and its state restored, when it is

re-powered. For a large modern computer system, these operations can take a very significant amount of time, during which the system is unavailable to its customers.

Computer manufacturers are well aware of the dependence of their customers, and have accordingly devoted considerable attention to these problems. As a result, many modern computer system have some degree of fault tolerance, and are capable of concurrent maintenance. Fault tolerance means simply that a single component of the computer system may fail without bringing the entire system down, although in some cases performance of the system or some other characteristic may be adversely affected. Concurrent maintenance is the capability to repair or replace some component of a computer system without shutting down the entire system, i.e., the system can continue to operate and perform useful work (although possibly in a diminished capacity) while the repair is being performed. A system which is both fault tolerant and capable of concurrent maintenance can, in theory, be kept running 24 hours a day for an indefinite length of time. In fact few, if any, systems achieve this level of reliability with respect to every component which may possibly fail.

One example of this type of fault tolerance is an array of storage devices known as a "RAID", i.e. redundant array of independent disks. A RAID stores data on multiple storage devices in a redundant fashion, such that any data can be recovered in the event of failure of any single storage device in the array. RAID's are usually constructed with rotating magnetic hard disk drive storage devices, but may be constructed with other storage devices such as optical drives, tape drives, etc. Various types of RAID's providing different levels of redundancy or other operating characteristics are described in a paper entitled, "A Case for Redundant Arrays of Inexpensive Disks (RAID)", by Patterson, Gibson & Katz, presented at the ACM SIGMOD

conference (June, 1988). Patterson, et al., classify five types of RAID, designated levels 1 through 5. The Patterson nomenclature has become standard in the industry. RAID
5 have proliferated to the point where an industry trade group called the RAID Advisory Board has attempted to establish standards for RAID characteristics. Further information regarding RAID can be found in *The RAIDbook, A Source Book for Disk Array Technology*, published by the RAID Advisory Board (5th Ed. February 1996).

10 Frequently, a RAID is manufactured and marketed as a stand-alone storage subsystem, which is housed in its own cabinet with its own power supply and supporting hardware and software, and which communicates through a standard
15 communications interface with a host computer system. Since it is desirable to make data available to the host system at all times, even if a single storage device in the RAID subsystem fails, the subsystem will frequently have its own on-board data recovery capability, which may include
20 temporary spare drives for storage of recovered data. The RAID subsystem may additionally have redundant power supplies or other redundant components.

Electronic systems frequently use backplane circuit cards for distribution of power, data signals, and/or
25 mounting of active or passive circuit elements and connectors. Such a card typically contains multiple parallel layers for embedded circuit patterns, grounds, or power distribution. Pluggable connectors couple the backplane to other modules which make up the electronic system, such as power supply modules, storage devices, or
30 logic cards. Often, such a backplane card acts primarily as a distribution medium, i.e., it conveys power and/or data signals from one module to another, and contains relatively few functional components attached directly to the backplane itself. However, the backplane may contain functional
35 components.

Because the backplane typically contains no moving parts and relatively few functional components, the probability of backplane failure is normally significantly less than the probability of failure of a disk drive storage device or a power supply. Accordingly, system designers have typically ignored the possibility of backplane failure, concentrating instead on such matters as the recovery of data from a non-functioning storage device. However, it is possible for a backplane to fail, albeit rarely. Because the backplane often forms the center of a web of communications among other modules, failure of the backplane, when it does occur, may be catastrophic. Systems are not typically designed to continue operation in the face of such a failure. Often, the system must be shut down, and numerous (perhaps all) modules in the system must be removed in order to replace a defective backplane. All of this takes precious time, time during which the system will be unavailable to its users.

As modern computer systems improve in sophistication and reliability, and users come to rely with greater dependence on the continuing availability of their systems, it is increasingly important to provide improved redundancy and concurrent maintenance capability in computer systems.

SUMMARY OF THE INVENTION

It is, therefore, an object of the present invention to provide an enhanced electronic system, and in particular to provide more robustness and increased concurrent maintenance capability in an electronic system having one or more backplane circuit cards.

An electronic system, such as a digital computer system or a subsystem thereof, contains a backplane circuit card assembly for supporting pluggable modules, and which distributes power and/or data signals among the pluggable

modules. The circuit card assembly includes a plurality of base cards connected by a data communications path, which provide redundant function in such a manner that any one base card and the pluggable module(s) connected to it may be removed and replaced while the system remains operational.

Preferably, the backplane comprises a pair of base cards, connected by a single smaller jumper card. The base cards are positioned in a single plane, with one edge of each base card lying near a corresponding edge of the other base card. The jumper is positioned parallel to and slightly offset from the base cards, the offset being needed to provide space for pluggable connectors which couple the jumper card to each of the base cards.

In the preferred embodiment, the electronic system includes multiple redundant power supply modules for supplying electrical power, and multiple redundant functional modules which perform some data processing function. Modules plug into the backplane card assembly using pluggable connectors capable of being unplugged and re-plugged. Each base card distributes electrical power from at least one power supply module directly coupled to it, i.e., without power coming through the other base card. Power is distributed via embedded power planes. Each base card further carries data signals among various modules coupled to it, independently of the other card. The power supply and functional modules directly attached to any one base card are sufficient to maintain minimum functionality of the electronic system, in the absence of the other base card.

The jumper preferably carries power and data signals between the two base cards. Thus, it is possible for electrical current generated by a power supply attached to one of the base cards to pass through the jumper and provide power to functional modules plugged into the other base

card. It is further possible for a functional module
attached to one base card to communicate through the jumper
with a functional module attached to the other base card.
Such capability may improve the performance of the system,
5 but it is not necessary for minimum system functionality.

In the preferred embodiment, the electronic system is
an intelligent redundant array of independent disks (RAID)
storage server, for providing data to multiple host digital
data processing systems. The storage server includes a
10 plurality of disk drives and power supplies housed within an
enclosure, together with supporting electronic logic cards
and other hardware. The various components provide
redundant operation in the event of a single failure.
Certain logic cards, power supplies and other modules plug
15 into the backplane circuit card, which functions as a
distribution medium for power and data signals. The design
of the server system, including the design of backplane
circuit card and couplings, supports concurrent maintenance,
whereby any component can be unplugged from the backplane
20 circuit card, removed from the enclosure, and replaced with
a functioning unit, all without powering down the subsystem.
Additionally, the backplane itself is split into multiple
circuit cards having duplicative function as described
above, so that concurrent maintenance can be performed on
25 the backplane itself.

The details of the present invention, both as to its
structure and operation, can best be understood in reference
to the accompanying drawings, in which like reference
numerals refer to like parts, and in which:

30 BRIEF DESCRIPTION OF THE DRAWING

Fig. 1 shows the major components of a RAID subsystem
using a backplane circuit card in accordance with the
preferred embodiment of the present invention.

Figs. 2A and 2B are isometric perspective views of the electronics drawer according to the preferred embodiment.

5 Figs. 3A and 3B are isometric perspective views of the left section backplane circuit card according to the preferred embodiment.

Figs. 4A and 4B are isometric perspective views of the right section backplane circuit card according to the preferred embodiment.

10 Figs. 5A and 5B are isometric perspective views of the jumper section circuit card, according to the preferred embodiment.

15 Fig. 6 is a cross-sectional view of a representative portion of one of the backplane circuit cards, showing the various conductive and non-conductive layers, according to the preferred embodiment.

20 Fig. 7 is a view of a voltage power plane for left section backplane circuit card, showing the location of islands and power ramps, according to the preferred embodiment.

Fig. 8 is a view of a voltage power plane for right section backplane circuit card, showing the location of islands and power ramps, according to the preferred embodiment.

25 Fig. 9 is a view of a voltage power plane for jumper section circuit card, according to the preferred embodiment.

Fig. 10 is enlarged partial view of a voltage power plane, showing a typical island and power ramp in greater detail, according to the preferred embodiment.

30 Fig. 11 is an exploded perspective view of the mounting frame for the electronics drawer, showing the mounting locations of the backplane circuit card assembly, according to the preferred embodiment.

DETAILED DESCRIPTION OF THE PREFERRED EMBODIMENT

35 Fig. 1 is a high level representation of the major components of server system 100 employing the backplane

circuit cards of the present invention, in accordance with the preferred embodiment. In this embodiment, server system is an intelligent and redundant mass repository of data, which it provides on demand to multiple host computer systems which are connected to server system 100. System 100 comprises a frame 101 for housing various electronic modules. These modules are shown in Fig. 1 in slide-out position for illustrative purposes, it being understood that during normal operation the modules are fully inserted in frame 101.

Near the top of the interior of frame 101 is electronics drawer assembly 102. Electronics drawer 102 provides the basic logic and memory functions for operation of system 100. The components and function of electronics drawer 102 are explained in greater detail below with respect to Fig. 2.

At the bottom of frame 101 is power drawer 103, which converts power from an external source to a voltage which is distributed to the various drawers. Specifically, power drawer 103 contains a pair of redundant power supplies, each power supply converting standard AC line voltage to 350 VDC for internal distribution within frame 101. Separate power distribution cables run from each power supply in power drawer 103 to electronics drawer 102 and each storage drawer 104-107, so that 350 VDC power is provided to each drawer in a redundant fashion, enabling system 100 to continue operation even if one of the 350 VDC power supplies fails..

Beneath electronics drawer 102 are several storage drawers 104-107, each of which may contain multiple rotating magnetic disk storage devices for storing data. The multiple rotating magnetic disk storage devices store data in a redundant fashion, according to one or more RAID levels. Additionally, electronics drawer 102 contains hardware and software necessary to reconstruct data stored

in any non-functioning disk storage device, and to store this data on a spare or replacement storage device. Thus, data stored on system 100 is available, even though the storage device on which the data was stored is itself
5 incapable of providing the data.

Figs. 2A and 2B show in greater detail the major interior functional components of electronics drawer assembly 102. Fig. 2A is an isometric perspective front view of electronics drawer 102, while Fig. 2B is an
10 isometric perspective rear view of drawer 102. In general, drawer 102 is housed in a frame (not shown) and includes left and right halves, the functional components of drawer 102 being duplicated in each half. Drawer 102 includes left power supply 201 and right power supply 202. These are
15 essentially identical units, each of which receives 350 VDC power from power drawer 103, and converts it to 48 VDC power for distribution within electronics drawer 102.

Drawer 102 further includes left card cage 203 and right card cage 204. Card cages 203, 204 are essentially
20 duplicates of each other, and house electronic circuit cards and other modules which perform the basic logical functions of system 100. Specifically, left cage 203 includes processor cards 241 which form the central processing unit (CPU), memory cards 243 which store programs and other data
25 for processing by the CPU, I/O cards 245 which provide an interface between I/O devices and modules and a system bus, diskette drive 251 and CD-ROM drive 253. Cage 203 further includes a rotating magnetic hard disk drive, which is not visible. Right cage 204 similarly includes processor cards
30 242, memory cards 244, I/O cards 246, diskette drive 252, CD ROM drive 254, and hard disk drive (not shown).

The rear side of drawer, visible in Fig. 2B, includes right and left I/O communication modules 209-212. I/O modules 209-212 communicate with one or more host computer

systems over a local area network or other medium, and/or communicate with storage devices located in drawers 104-107, handling the transfer of data between drawer 102 and the storage devices. I/O modules 209-212 may optionally
5 communicate with additional storage devices located in drawers in adjacent racks (not shown). Data communication cables (not shown) exit the rear of modules 209-212 to connect the modules with their respective hosts, storage devices, or other devices.

10 Drawer 102 further includes reserved spaces for later system enhancements, depicted in Figs 2A and 2B as modules 205-208 and 213-216. Modules 213-216 are intended as additional I/O communications modules, similar in function to modules 209-212. Modules 205 and 206 are intended as
15 optional performance enhancement modules, which may include cache and other logic to increase the performance of the storage subsystem. Modules 207 and 208 are intended as additional non-volatile random access memory (NV RAM), which temporarily store data bound for storage devices located in
20 storage drawers 104-107. Modules 205-208 and 213-216 are shown in Figs. 2A and 2B although in fact they are not supported by the backplane card design of the preferred embodiment. Support for these modules may require
25 modifications to backplane sections 220 and 221 so that the backplanes would mount connectors supplying power and data signals to the modules.

Modules are electrically connected to one another for distribution of power and data signals by means of a three-part system backplane, comprising left backplane
30 circuit card section 220, right backplane circuit card section 221, and backplane jumper card section 222. As used herein, the term "module" includes any electronic subassembly, such as circuit cards and card cages, as well
35 as fully enclosed modules. Left section 220 and right section 221 are similar, and provide essentially duplicative

function. I.e. left section 220 couples the various modules located on the left side of drawer 102, providing power to the modules and permitting data to flow between modules. Right section accomplishes the analogous task for the modules on the right side of drawer 102. Jumper section 222 is smaller than either section 220 or 221. Jumper 222 electrically connects the other two sections, allowing signals to pass between them and distributing power, and provides grounding connections, as explained further herein. Dividing the backplane into two separate sections linked by a jumper, each having duplicative function, makes it possible to replace one of the backplane sections while the other continues to function. While it would have been possible (and generally cheaper) to provide a single larger backplane circuit card, the three part design enhances the concurrent maintenance capability of the system.

The two functional halves of electronics drawer 102 are capable of independent operation, although they are linked by a data communication path across jumper 222. Each half has its own independent connection with a host system, so that each half can independently receive or transmit data from the host. Each has its own independent power. Each has its own independent bus connection to the storage devices. Preferably, the storage devices buses are configured as inherently redundant loops using IBM's Serial Storage Architecture, each bus being coupled to bus interfaces in both halves of electronics drawer 102, so that either half of the drawer can access any arbitrary storage device. Finally, each half of drawer 102 has its own processor and memory for processing commands from a host, issuing commands to storage devices, transmitting and caching data, performing parity or other error correction and detection calculations, performing error recovery and diagnostics, and other subsystem functions. In normal operating mode, storage access requests from a host will be allocated between the two halves based on the storage device

accessed, address range, or other criteria, so that ideally each half is doing roughly equal work. Each half will thus independently handle a portion of the data access. The communications path between halves may be used for cache coherency, parity calculations, status information, diagnostics, etc. In the event any half of drawer 102 is non-functional for any reason, the remaining half will handle all data access requests from the host to any storage device. Because there will only be one processor and associated hardware to handle all storage access requests (where there are normally two), this may result in lowered performance. However, the system will remain operational.

Figs. 3A and 3B are perspective views of left section backplane circuit card 220 according to the preferred embodiment. Fig. 3A shows the front side of card 220, while Fig. 3B shows the back side. On the back side and near the top of card 220 can be seen a pair of power connectors 301. Beneath power connectors 301 is a set of three signal connectors 302. Power connectors 301 and signal connectors 302 mate with corresponding connectors 501 and 502 respectively on jumper card 222 to form the connection with the jumper card. Additional connectors mate with corresponding connectors on the various modules. Specifically, power supply connectors 303 mate with left power supply 201; card cage connectors 305 mate with connectors of a power supply (not visible) in left card cage 203 which provides power at working voltages and status information for main processor cards 241, memory cards 243, various I/O cards 245, etc.; I/O card connectors 306 mate with connectors in card cage 203 for the various I/O cards 245; and I/O module connectors 307 mate with the left I/O adapter modules 211 and 212. Embedded signal lines are formed in a plurality of conductive planes in circuit card 220, running between the various connectors. Holes 310 in card 220 allow cooling air to pass through the backplane.

Figs. 4A and 4B are perspective views of the right section of backplane circuit card 221, according to the preferred embodiment. Circuit card 221 is generally similar in construction and function to card 220. Fig. 4A shows the front side of card 221, while Fig. 4B shows the back side. Power connectors 401 and signal connectors 402 form connections to connectors 503 and 504 respectively of jumper card 222, similarly to left section card 220. Power supply connectors 403 mate with right power supply 202; card cage connectors 405 mate with connectors of a power supply (not visible) in right card cage 204 which provides power at working voltages and status information for main processor cards 242, memory cards 244, various I/O cards 246, etc.; I/O card connectors 406 mate with connectors in card cage 204 for the various I/O cards 246; and I/O module connectors 407 mate with right I/O adapter modules 209 and 210. Embedded signal lines are formed in a plurality of conductive planes in circuit card 221, running between the various connectors. Holes 410 permit the passage of cooling air.

Figs. 5A and 5B are perspective views of jumper section circuit card 222, according to the preferred embodiment. Jumper section is located in the center of electronics drawer 102, straddling the two circuit cards 220, 221. Power connectors 501 and 503 are electrically connected to each other via embedded power planes to transmit power between the connections to left section 220 and right section 221 respectively. Various pins in signal connectors 502 and 504 are likewise electrically connected to each other via a plurality of discrete embedded signal lines in jumper card 222, providing a communications path for data and other signals between the left and right circuit cards. Ground plugs 521 and 522 provide redundant locations for the attachment of a ground bus to the frame of drawer 102, which is in turn grounded to frame 101.

Power monitor connectors 510 and 511 receive the mating connectors of corresponding redundant cables (not shown) which run between jumper 222 and redundant 350 VDC power supplies in power drawer 103. These connectors and cables do not carry power, but carry data signals used for monitoring and regulating power. Specifically, power is continually regulated so that, under normal operating conditions, power supply 201 provides all power to modules attached to circuit card 220, while power supply 202 provides all power to modules attached to circuit card 221. The power provided by these power supplies is thus balanced, so that no power current is flowing through the jumper from one half of the backplane to another. However, where one power supply fails, its redundant neighbor will supply power to the other half of the backplane through the jumper.

Each of cards 220, 221 and 222 comprise multiple alternative electrically conductive and non-conductive layers. These layers are constructed using any of various conventional methods known in the art. Non-conductive layers are constructed of a suitable fiberglass epoxy material. Conductive layers are constructed of copper foil, which is etched, drilled, or otherwise formed to an appropriate pattern. Conductive layers can be broadly divided into two categories: (a) signal layers, and (b) power and ground layers. Signal layers typically contain conductive patterns in the form of many relatively narrow electrically isolated signal lines, each line running between two or more locations (such as pins or vias) in the plane. Power and ground layers typically contain only a single electrical pattern for supplying voltage and/or providing impedance matching. A power or ground layer is typically a broad sheet of substantially uniform thickness and conductivity occupying nearly all of the surface area of the plane, with only small areas around pins or vias removed to insulate the pin or via from the voltage of the power/ground layer.

Fig. 6 is a cross-sectional view of a representative small portion of one of backplane circuit cards 220, 221 or 222, according to the preferred embodiment. Conductive layers 601-618 alternate with non-conductive (dielectric) layers 621-637. Layers 603, 604, 606, 607, 612, 613, 615, and 616 are internal signal layers, i.e., signal layer planes which are buried in the middle of the circuit card. Layers 601 and 618 are signal layers lying on either surface of the card. Ground layer planes 602, 605, 608, 611, 614 and 617 provide impedance control for adjacent signal layers.

In the middle of the card lie two power layer planes 609, 610. Power plane 609 carries +48 VDC power supply voltage, while power plane 610 is 0 VDC power return voltage. While the grounds are also nominally 0 V, plane 610 is different from the ground planes because plane 610 carries substantial DC electrical current required for providing power to the various modules. Accordingly, both power planes 609 and 610 are substantially thicker than the signal planes or the ground planes. Specifically, in the preferred embodiment, internal signal planes and ground planes have a nominal design thickness of 0.03mm, while power planes 609 and 610 have a nominal design thickness of 0.064mm. Surface signal planes 601 and 618 are thicker than internal signal planes, having a nominal design thickness of 0.058mm.

Each plane is depicted in Fig. 6 as a continuous, uniform thin sheet for purposes of illustrating the relative locations and thicknesses of the layers only. It will be understood that in fact discontinuities exist in various places, and particularly that signal layers comprise multiple discrete lines which are not shown in Fig. 6. Furthermore, it will be understood that various vias exist in cards 220-222 for connecting one plane with another. Signal vias generally connect a signal line in one plane

with a signal line in another plane. Generally, for each signal via there is also an adjacent ground via which connects the ground plane adjacent the plane of one signal line with the ground plane adjacent the plane of another signal line, for impedance matching purposes.

Except in jumper 222, ground vias are insulated from the 0 VDC power plane. I.e., on cards 220 and 221, ground planes 602, 605, 608, 611, 614 and 617 are not electrically connected to 0 VDC power plane 610. The tying of 0 VDC power to ground is accomplished in jumper 222. Specifically, ground plugs 521 and 522 include pins extending through the width of jumper 222, which are electrically connected to each ground plane and to 0 VDC. Additionally, a row of ground vias 523 runs down the center of card 222 between plugs 521 and 522, each via electrically connecting all ground planes and the 0 VDC power plane. Each ground plane within the left 220 or right 221 half backplane is electrically connected to its corresponding ground plane in jumper 222 through pins in connectors 302, 402, 502 and 504. Thus, the 0 VDC is electrically isolated from the ground planes everywhere on the backplane assembly except down the middle of the jumper (by "electrically isolated", it is meant that there exists no direct electrical connection having negligible impedance, although there may be current paths through components having significant though finite impedance).

Because jumper 222 is substantially symmetrical, and power is balanced between the two halves of the backplane as described above, the power plane voltage potential along row of vias 523 and ground plugs 521, 522, should be equal. Thus, any recirculating electrical current due to ground loops is effectively minimized. For purposes of minimizing ground loop current, this design is similar to a single point of grounding the 0 VDC power plane.

Fig. 7 is a view (from rear) of a power plane for left section circuit card 220. As can be seen by comparing Fig. 7 to Figs. 3A and 3B, the power plane generally follows the contours of card 220, and contains air flow holes 710 corresponding to holes 310 in card 220. The power plane includes a plurality of smaller holes for connector pins, which appear in Fig. 7 as rectangular groups or arrays of black dots. Some of these holes are slightly larger than corresponding physical pin holes in the card, creating a small insulating void around the pin to electrically isolate the pin from the power plane. At other pin holes, the plane extends all the way to the pin hole, creating an electrical contact with the pin when the hole is filled with solder during assembly of the circuit card. Additional isolated scattered holes may be signal via holes or mounting holes.

Both the +48 VDC power plane and the 0 VDC power plane for a particular circuit card are nearly identical. Both planes are identical with respect to the outer edges, the locations of all via holes for signal vias, ground vias and power vias of other voltages, all mounting and cooling holes, and the edges of all non-conductive voids which define the boundaries of ramps and islands. The only differences between the two planes are the identity of particular connector pin locations which are electrically coupled or insulated. I.e., each via which forms a fill or connection with one power plane is always surrounded by an insulating void at the other power plane. These differences are too small to see in Fig. 7, and so Fig. 7 may be regarded as either the +48 VDC plane or the 0 VDC plane. The same observations are true of Figs. 8 and 9, further described below.

Hole group 701 corresponds to power connector 301, each hole accommodating a respective pin from the connector. Hole group 702 corresponds to signal connector 302 to jumper. Similarly, hole groups 703, 705, 706 and 707

correspond to connectors 303, 305, 306 and 307, respectively.

To the left of each of hole groups 707 will be seen a horseshoe shaped line surrounding two columns of holes. A similar but much larger structure surrounds hole group 705. These lines represent voids in the power plane. These voids effectively create small conductive areas in the vicinity of the holes, which are isolated from the rest of the power plane at most of their perimeter, and electrically coupled to the power plane only at a relatively narrow connecting area. These conductive areas are referred to herein as "islands", and the narrow connecting areas as "ramps". The main portion of the power plane, which surrounds the islands, is referred to as the "body". The body of the plane distributes current among the islands. Specifically, it distributes power from the power supply island to the islands associated with the various power consuming modules, including the card cage (the direction of current flow depending on the polarity of the voltage in the power plane).

Void 712 nearly surrounds hole groups 701 and 703, and defines a ramp to the right of group 703. The comparatively large island defined by this void encompasses all the power supply connections. I.e., hole group 703 corresponds to the connector to power supply 201, which normally supplies power to backplane section 220. In the event of failure or absence of supply 201, power to backplane section 220 is provided by power supply 202, through the connections in jumper card 222. This power is routed through power connector 301, corresponding to hole group 701. Thus the power to card 220 is always routed through the island defined by void 712, regardless of which power supply is actually providing it. Conversely, if power supply 202 should fail, requiring power supply 201 to double its output in order to supply the power needs of card 221 and its

attached modules, the additional current is routed through the island defined by void 712, without traversing the body of the power planes in card 220.

5 The island and ramp configuration is used around pins which are electrically coupled to one of the power planes. These are the connections which supply electrical power to the modules. Because power is being supplied to modules through these pins, they normally conduct a relatively high electrical current in comparison to pins used for
10 transmitting data signals. It will further be observed that typically an array of power pins is contained in each island, although only two electrical voltages (+48 VDC and 0 VDC) are used for power distribution. Multiple pins are used to supply power from the same power plane, through the
15 same connector, to the same module. This is done in order to reduce the electrical current through each individual pin.

Fig. 8 is a view of a power plane for right section circuit card 221. As can be seen by comparing Fig. 8 to
20 Figs. 4A and 4B, the power plane generally follows the contours of card 221, and contains air flow holes 810 corresponding to holes 410 in card 221. The general construction and layout is similar to that of left card 220. As in the case of the power plane for left circuit card 220,
25 the power plane for right card 221 includes a plurality of smaller holes for connector pins, which appear in Fig. 8 as groups or arrays of black dots, some of which depict insulating voids while others depict connections to the via.

Hole group 801 corresponds to power connector 401, each
30 hole accommodating a respective pin from the connector. Hole group 802 corresponds to signal connector 402 to jumper. Similarly, hole groups 803, 805, 806 and 807 correspond to connectors 403, 405, 406 and 407, respectively.

Horseshoe shaped voids defining islands and ramps will be seen near hole groups 807, and surrounding hole group 805. Similarly to left card 220, void 812 defines a single large island and ramp encompassing hole groups 801 and 803, which correspond to the power connectors.

Figure 9 shows a power plane for jumper section 222. As can be seen by comparing Fig. 9 to Figs. 5A and 5B, the power plane is the same shape as jumper card 222, and contains connector pin holes corresponding to the various features of the jumper, which appear as black dots on the power plane of Fig. 9. Hole group 901 corresponds to power connector 501, each hole accommodating a respective pin from the connector. Hole group 803 corresponds to power connector 503 for the opposite side. Similarly, hole groups 902, 904, 910, 911, 921 and 922 correspond to connectors 502, 504, 510, 511, 521 and 522, respectively. Surrounding power hole groups 901 and 903 are respective voids 912 defining islands and ramps.

A row of holes 923 runs down the middle of the power plane, corresponding to grounding vias 523. On the 48 VDC power plane, all these holes are insulated from the via. On the 0 VDC power plane, all these holes represent connections to the grounding via, which electrically connects the 0 VDC power plane with the various ground planes. Similarly, hole groups 921 and 922 also represent grounds, being connected to the chassis ground and to each ground plane. In the 48 VDC power plane, hole groups 921 and 922 are insulated from the pins within, while in the 0 VDC power plane, these hole groups represent electrical connections to the pins.

The island and ramp configuration helps to reduce the effect of transients, particularly those caused by unplugging or plugging a module. For example, without the island and ramp, if a module which is powered on (i.e., receiving power) is suddenly unplugged from the backplane,

so that power is abruptly disconnected, there is a sudden large change in the electrical current density function for the power plane, which is particularly acute in the vicinity of the pins which supply power to a module. Similarly,
5 plugging a powered-off module into the backplane causes a large current spike (and possible arcing) as a result of sudden application of voltage to what is effectively a large discharged capacitor. The resulting induced magnetic field can corrupt data signals in one or more of the signal
10 planes.

The island and ramp configuration reduces these effects in several ways. The two power planes are preferably of identical patterns (except at connections to the power pins), separated by a single dielectric layer. Channelling
15 all the current through power ramps which are mirrored in the two power planes causes a high degree of coupling of pairs of power ramps in different respective planes, significantly reducing electromagnetic field effects of the current surges. Moreover, the use of power ramps themselves
20 slows the change in current by increasing the impedance of the plane in the vicinity of the pins.

Fig. 10 illustrates a typical island, ramp and pin connections. Island 1001 is the area defined by a circular void pattern 1002, having an opening and parallel voids
25 1003, 1004 extending from the opening. The area between parallel voids 1003, 1004 is the ramp 1005. In this example, eight power pin holes 1011-1018 are within island 1001, each hole corresponding to a connector pin used for transmitting electrical power. Pins through four of the
30 holes 1011-1014 provide power at the voltage of the plane containing island 1001, and are therefore electrically coupled to island 1001. The remaining four pins through holes 1015-1018 are electrically insulated from island 1001, and electrically coupled to a mirror image of island 1001 in
35 the other power plane.

Preferably, certain parameters are observed in the design of islands and ramps. The length of ramp 1005 (i.e., the length of either void 1003 or 1004) is preferably the greater of either: (a) the distance between ramp 1005 and the nearest power pin hole 1011; or (b) twice the width of ramp 1005. The width of ramp 1005 is determined by the acceptable temperature rise in the power layers due to the current density, i.e., ramp 1005 is made as narrow as possible given the constraints of current and temperature rise. The shape of the island is not particularly significant; although a circular island is shown in Fig. 10, the island could be oblong, rectangular, or other shapes. The ramp is preferably of constant width, and surrounds no vias, mounting or cooling holes.

In order to maximize electro-magnetic coupling between the two power layers 609, 610, it is not only preferable that they be of substantially identical patterns (except at the power pin connections), but that the physical distance (i.e., dielectric layer 629) between the two power planes be as small as possible, without risking dielectric breakdown. Furthermore, in order to isolate the signal planes from power planes, it is preferred that ground planes 608, 611 be placed on either side of the pair of power planes, and that the insulating layers in this region be relatively thick. However, in the design of any circuit card, these parameters must be regarded as goals rather than absolutes, and it is recognized that other design constraints may impose countervailing requirements.

For example, in the backplane circuit card of the preferred embodiment, dielectric layer 629 separating the two power planes is approximately 0.124mm thick. This is actually only slightly thinner than some other dielectric layers in the circuit card, but in view of the fairly large power load being carried by the power planes, such a dimension is deemed desirable. Where a card is used to

5 distribute smaller power loads or other parameters are
different, it may be possible to design a thinner insulating
layer between the power planes. Dielectric layers 628 and
630, separating the power planes from ground planes 608 and
611, are each approximately 0.130mm thick, while dielectric
10 layers 627 and 631, separating ground planes 608 and 609,
respectively, from adjacent signal planes, are each
approximately 0.188mm thick. These dimensions do not
necessarily represent an ideal from the standpoint of
15 electro-magnetic transient isolation, but were chosen in
order to achieve other goals as well, such as
manufacturability and cost.

Each pair of matching islands in the two power planes
surrounds all the +48 VDC and 0 VDC connections to a
15 particular module. Accordingly the amount of current
carried by each island is exactly the same as the current
carried by the matching island in the other voltage plane.
Because the conductive path through the ramp is relatively
long and narrow, asymmetrical current distribution within
20 the island is largely dispersed by the time current reaches
the opening at the end of the ramp to the body of the power
plane, and current densities at these locations of the two
power planes are equal in magnitude and opposite in
direction. Because the bodies of the two power planes are
25 dimensionally nearly identical, and current densities at the
openings to the power ramps are equal in magnitude yet
opposite in direction, current densities remain equal in
magnitude and opposite in direction everywhere within the
body of the power planes.

30 Because electro-magnetic effects are reduced by
coupling the two power layers, it is important that the
magnitude of current in each be the same. Therefore, all
return current to the power supplies should be carried in
the 0 VDC power plane 610. Any current leakage into the

ground planes 602, 605, 608, 611, 614 and 617 reduces the effectiveness of coupling the two power layers.

5 In order to further improve resistance to transients, it is preferred that islands and ramps be kept relatively small, and that signal lines avoid passing directly over or under the islands and ramps, to the extent possible. It is within the islands that the current densities will be asymmetrical, producing the largest resultant
10 electromagnetic forces during rapid current changes, e.g., as caused by hot plugging. To this end, these areas may be designated "keep outs" during design of the signal planes. I.e., signal wires are not routed through such areas.

Fig. 11 is an exploded perspective view of the mounting frame and associated hardware for electronics drawer 102, according to the preferred embodiment. The mounting frame
15 includes U-shaped bottom-and-side section 1101 and top section 1102, both of which are formed sheet steel containing appropriate mounting holes, tabs and other features. The backplane assembly, comprising cards 220, 221 and 222, is positioned approximately in the center of
20 section 1101, perpendicular to the sides and bottom. A sheet steel stiffener 1103 is attached to section 1101, and positioned parallel to cards 220, 221 and 222. Stiffener 1103 contains holes for cooling airflow corresponding to the various holes in cards 220 and 221. Stiffener 1103 also
25 contains holes for various connectors mounted on cards 220, 221 and 222. Cards 220 and 221 are mounted on the front side of stiffener 1103, while card 222 is mounted on the rear side. Thus, stiffener 1103 lies in a plane between the jumper and the two functional halves. A thin plastic
30 insulator (not shown) prevents electrical contact between cards and stiffener.

Cards 220 and 221 are mounted to stiffener 1103 and section 1101 with screws, at the locations indicated.

Screws along the sides of section 1101 are installed through holes in cards 220, 221 and stiffener 1103, threading into tabs 1104 on section 1101. Either card 220 or 221 can be removed independently of the other, by first sliding all
5 modules in its respective half of the drawer out the front or rear, unscrewing the mounting screws from the stiffener and section 1101, and then pulling the card forward, thereby disconnecting it from the jumper.

Grounding bus bar 1105 forms a frame ground for the
10 backplane assembly. Ground bar 1105 is a solid metal bar attached to jumper 222 via screws at plugs 521 and 522, the two screw locations providing redundant attachment to the jumper. Bar 1105 is also attached to top section 1102 via two redundant screws. This arrangement of ground bar 1105
15 also provides additional structural support to the backplane circuit card assembly.

In the preferred embodiment described above, jumper card 222 is non-redundant and constitutes a possible single point of failure for the system. Because jumper 222
20 contains no attached components other than connectors (contains no active devices, resistors, capacitors, etc.), the probability of jumper failure is considered extremely low, and accordingly this portion of the system is non-redundant. However, it would have alternatively been
25 possible to design the system in such a way that the jumper itself could be concurrently maintained and replaced. There are several ways in which this could have been done. It would have been possible to construct the backplane with a second jumper card, located between the left and right
30 circuit cards below jumper 222. In this case, it would not have been necessary to place both power monitor connectors 510 and 511 on jumper 222; one such connector would be on jumper 222, while the other would be on the second jumper (the system being capable of operation with only one such
35 power monitor connection). In another alternative, it would

have been possible to remove critical function from jumper 222 so that the system could function without any jumper. For example, the power monitor connectors 510 and 511 could have been relocated to the left and right circuit cards 220 and 221; and the 0 VDC, ground planes and chassis ground could have been electrically tied together at locations on left and right circuit cards 220 and 221. A third alternative would have been a combination of the first two: relocate power monitor connectors 510 and 511 to the left and right circuit cards respectively, and construct a second jumper card having extremely limited function. I.e., the second jumper would only connect the 0 VDC, ground planes, and chassis ground, without any transmission of data. Because each half of the backplane is independently capable of supporting a system, data running between the two halves is not necessary for minimal operation. However, in any implementation which would disrupt communications between the two halves during concurrent maintenance, it would be necessary for control software to temporarily shut down one half of the system or otherwise guarantee re-synchronization of the two parts of the system after data communication is restored.

In the preferred embodiment, the left and right functional halves of the backplane are joined by a smaller jumper card. The jumper facilitates maintenance operations by allowing either left or right portion of the backplane to be withdrawn from the front of electronics drawer 102, without disturbing the card or modules of the other half. I.e., it is possible to remove a base card by pulling it directly forward, in a direction substantially perpendicular to the plane of the card. During such an operation, all modules of the opposite base card remain in place. The jumper also contains the ground connections, thus confining ground connections to a small area and reducing parasitic current through the ground planes. However, it would be possible to construct a redundant system including a

redundant, multi-part backplane, without such a jumper. For example, appropriate edge connectors could be used to join left and right sections directly, without the intermediate jumper. In this case, concurrent maintenance (i.e., card removal) would be performed by moving the card sideways, requiring appropriate modifications to drawer 102, frame 101, and/or other assemblies. It would also be necessary to form ground connections on the base cards themselves, which could increase the parasitic current in the ground planes.

In the preferred embodiment, the power planes are located in the middle of the various layers which make up the circuit card, with signal planes located on either side of the power planes. This arrangement buries the power planes far from components on either side of the backplane. However, it would alternatively be possible to locate the power planes near a surface of the card, with all signal planes being located to one side of the power planes. This arrangement may prove advantageous if, e.g., connectors and other components are located on only one side of the backplane.

In the preferred embodiment, the backplane is used only for distribution of power and signals among pluggable components, and itself contains no active functional electronic components. However, it would alternatively be possible to construct multi-part backplanes as described herein, which also carry functional components such as processors, switches, programmable logic arrays, etc. In order to assure redundancy, it may be necessary to duplicate functional components on different sections of the backplane.

In the preferred embodiment, a backplane circuit card for power distribution is designed to pluggably receive both the modules which supply power and the modules which consume power. However, it would alternatively be possible to

construct systems in which any single backplane receives only modules of a single type. For example, a backplane may have several pluggable couplings for receiving disk drive modules (consumers of power), and be connected by flexible
5 cable or any of various other conventional connection means to one or more power supplies, or to a second backplane into which one or more power supplies are plugged. Similarly, multiple power supplies may plug into a backplane to form a power bus, which is connected to the power consumers by
10 cable or other means.

In the preferred embodiment, a pair of power planes is used to distribute power at a voltage somewhat higher than normally used by semiconductor circuits. The higher
15 distribution voltage (48 volts) reduces current in the distribution medium. It is expected that each module will have circuitry necessary to produce working voltages such as 5 volts. However, it would alternatively be possible to construct a system in which all working voltages are distributed on the power planes, or in which a combination
20 of higher distribution voltages and working voltages are distributed on the power planes. In this case, more than two power planes may be necessary. For example, a backplane circuit card may have three power planes, for 0, +5 and +12 volts, each power plane having corresponding islands as
25 described herein. As an alternative example, the backplane may contain power planes at +48VDC distribution voltage, 0 VDC return distribution voltage, +5 VDC working voltage, and 0 VDC return working voltage.

In the preferred embodiment, each 48 VDC power supply
30 is allocated to one of the halves of the backplane and under normal conditions supplies all its power. However it would alternatively be possible to construct the system having two or more power supplies coupled to a common power bus, each half of the backplane having its independent connection to
35 the bus, so that no one power supply is directly associated

with any section of the backplane. It would further be possible to have multiple redundant power buses.

5 In the preferred embodiment, the backplane contains two redundant base circuit cards joined by a jumper. Because the cards are paired, each one duplicates the essential functions of the other. However, it would be possible to construct a backplane having more than two base circuit cards, where the system may function without any single one of the base cards.

10 In the preferred embodiment, pins which carry power to or from the backplane are surrounded by islands and ramps in order to reduce the effects of transients during hot plugging. However, islands and ramps are not strictly necessary, and it would alternatively be possible to
15 construct multi-part backplanes as described herein without island or ramps. Such a backplane may be constructed, e.g., without power connections; or with power connections having other forms of transient suppression such as active circuit elements; or with power connections which are shut down
20 before insertion and removal of modules so that hot plugging is not necessary; or employing a system design which in some other way modifies or tolerates transients resulting from hot plugging.

25 Although a specific embodiment of the invention has been disclosed along with certain alternatives, it will be recognized by those skilled in the art that additional variations in form and detail may be made within the scope of the following claims:

WE CLAIM:

CLAIMS

- 1 1. An electronic system, comprising:
2 a frame for housing a plurality of electronic modules;
3 a backplane circuit card assembly mounted to frame,
4 said backplane circuit card assembly comprising:
5 (a) a plurality of base circuit cards, and
6 (b) at least one jumper circuit card, said at least one
7 jumper card being pluggably coupled to said plurality
8 of base circuit cards and providing a plurality of
9 electrically conductive paths between base circuit
10 cards; and
11 a plurality of electronic modules mounted to said frame
12 for redundantly performing at least one function necessary
13 for operation of said electronic system, each of said
14 electronic modules being pluggably coupled to a respective
15 base circuit card;
16 wherein, with respect to each of said plurality of base
17 circuit cards, it is possible to remove the respective base
18 circuit card from said electronic system and replace the
19 respective base circuit card while said electronic system
20 remains operational, the remainder of said plurality of base
21 circuit cards providing redundant function for the
22 respective base circuit card being removed.
- 1 2. The electronic system of claim 1, wherein said
2 plurality of base circuit cards are mounted in substantially
3 coplanar configuration, and said at least one jumper card is
4 mounted parallel to and offset from said base circuit cards.
- 1 3. The electronic system of claim 1, wherein each of said
2 plurality of base circuit cards and said at least one jumper
3 card contains a respective set of embedded power planes for
4 transmitting electrical power, each set of embedded power
5 planes containing a plurality of power planes, each power
6 plane of said plurality of power planes in a set for

7 carrying an electrical voltage at a respective single pre-
8 defined voltage level.

1 4. The electronic system of claim 1, wherein:

2 said frame forms the outlines of a substantially
3 rectangular parallelepiped;

4 said plurality of base circuit cards are mounted in
5 substantially coplanar configuration, substantially
6 perpendicular to the top, bottom and sides of said frame,
7 and dividing the interior of said parallelepiped into front
8 and rear regions;

9 a first plurality of said electronic modules are
10 mounted in said front region; and a second plurality of
11 said electronic modules are mounted in said rear region.

1 5. The electronic system of claim 4, wherein:

2 said plurality of base circuit cards includes a left
3 base circuit card and a right base circuit card, said left
4 and right base circuit cards being mounted within a left
5 half of the interior of said parallelepiped and a right half
6 of the interior of said parallelepiped, respectively;

7 said plurality of electronic modules includes a
8 plurality of left side electronic modules which plug into
9 said left base circuit card, and a plurality of right side
10 electronic modules which plug into said right base circuit
11 card;

12 said plurality of left side electronic modules includes
13 at least one left power supply module for supplying power to
14 said left base circuit card; and

15 said plurality of right side electronic modules
16 includes at least one right power supply module for
17 supplying power to said right base circuit card.

1 6. The electronic system of claim 1, further comprising:

2 a plurality of power supply modules, each power supply
3 module being associated with a respective base circuit card

4 and supplying power to the base circuit card with which it
5 is associated.

1 7. The electronic system of claim 6, wherein said jumper
2 circuit card further comprises power couplings for
3 transmitting power between said base circuit cards, whereby
4 in the event of failure of a power supply associated with a
5 first base circuit card, power may be supplied to said first
6 base circuit card by at least one power supply associated
7 with another base circuit card, said power being transmitted
8 through said jumper circuit card.

1 8. An electronic system, comprising:
2 a frame for housing a plurality of electronic modules;
3 a backplane circuit card assembly mounted to said
4 frame, said backplane circuit card assembly comprising a
5 plurality of base circuit cards having pluggable couplings
6 for supporting a communications path between base circuit
7 cards;
8 a plurality of electronic modules mounted to said frame
9 for redundantly performing at least one function necessary
10 for operation of said electronic system, each of said
11 electronic modules being pluggably coupled to a respective
12 base circuit card;
13 wherein, with respect to each of said plurality of base
14 circuit cards, it is possible to (a) remove each of said
15 electronic modules pluggably coupled to the respective base
16 circuit card from said electronic system, and (b) remove the
17 respective base circuit card itself from said electronic
18 system, said removal of said electronic modules and base
19 circuit cards being performed while said electronic system
20 remains operational, the remainder of said plurality of base
21 circuit cards and electronic modules providing redundant
22 function for the respective base circuit card and electronic
23 modules being removed.

1 9. The electronic system of claim 8, wherein:

2 said frame forms the outlines of a substantially
3 rectangular parallelepiped;

4 said plurality of base circuit cards are mounted in
5 substantially coplanar configuration, substantially
6 perpendicular to the top, bottom and sides of said frame,
7 and dividing the interior of said parallelepiped into front
8 and rear regions;

9 a first plurality of said electronic modules are
10 mounted in said front region; and a second plurality of
11 said electronic modules are mounted in said rear region.

1 10. The electronic system of claim 9, wherein:

2 said plurality of base circuit cards includes a left
3 base circuit card and a right base circuit card, said left
4 and right base circuit cards being mounted within a left
5 half of the interior of said parallelepiped and a right half
6 of the interior of said parallelepiped, respectively;

7 said plurality of electronic modules includes a
8 plurality of left side electronic modules which plug into
9 said left base circuit card, and a plurality of right side
10 electronic modules which plug into said right base circuit
11 card;

12 said plurality of left side electronic modules includes
13 at least one left power supply module for supplying power to
14 said left base circuit card; and

15 said plurality of right side electronic modules
16 includes at least one right power supply module for
17 supplying power to said right base circuit card.

1 11. The electronic system of claim 8, wherein said
2 backplane circuit card assembly further comprises at least
3 one jumper circuit card, said jumper circuit card having
4 pluggable couplings for mating with the pluggable couplings
5 for supporting a communications path of at least two of said
6 base circuit cards, said jumper circuit card further having
7 circuit paths running between said pluggable couplings and

8 forming a portion of said communications path between said
9 base circuit cards.

1 12. The electronic system of claim 11, wherein said
2 plurality of base circuit cards are mounted in substantially
3 coplanar configuration, and said at least one jumper card is
4 mounted parallel to and offset from said base circuit cards.

1 13. The electronic system of claim 8, further comprising:
2 a plurality of power supply modules, each power supply
3 module being associated with a respective base circuit card
4 and supplying power to the base circuit card with which it
5 is associated.

1 14. The electronic system of claim 13, wherein said jumper
2 circuit card further comprises power couplings for
3 transmitting power between said base circuit cards, whereby
4 in the event of failure of a power supply associated with a
5 first base circuit card, power may be supplied to said first
6 base circuit card by at least one power supply associated
7 with another base circuit card, said power being transmitted
8 through said jumper circuit card.

1 15. The electronic system of claim 8, wherein each of said
2 plurality of base circuit cards contains a respective set of
3 embedded power planes for transmitting electrical power,
4 each set of embedded power planes containing a plurality of
5 power planes, each power plane of said plurality of power
6 planes in a set for carrying an electrical voltage at a
7 respective single pre-defined voltage level.

1 16. The electronic system of claim 8, further comprising:
2 a plurality of power supply modules, each power supply
3 module being associated with a respective base circuit card
4 and supplying power to the base circuit card with which it
5 is associated.

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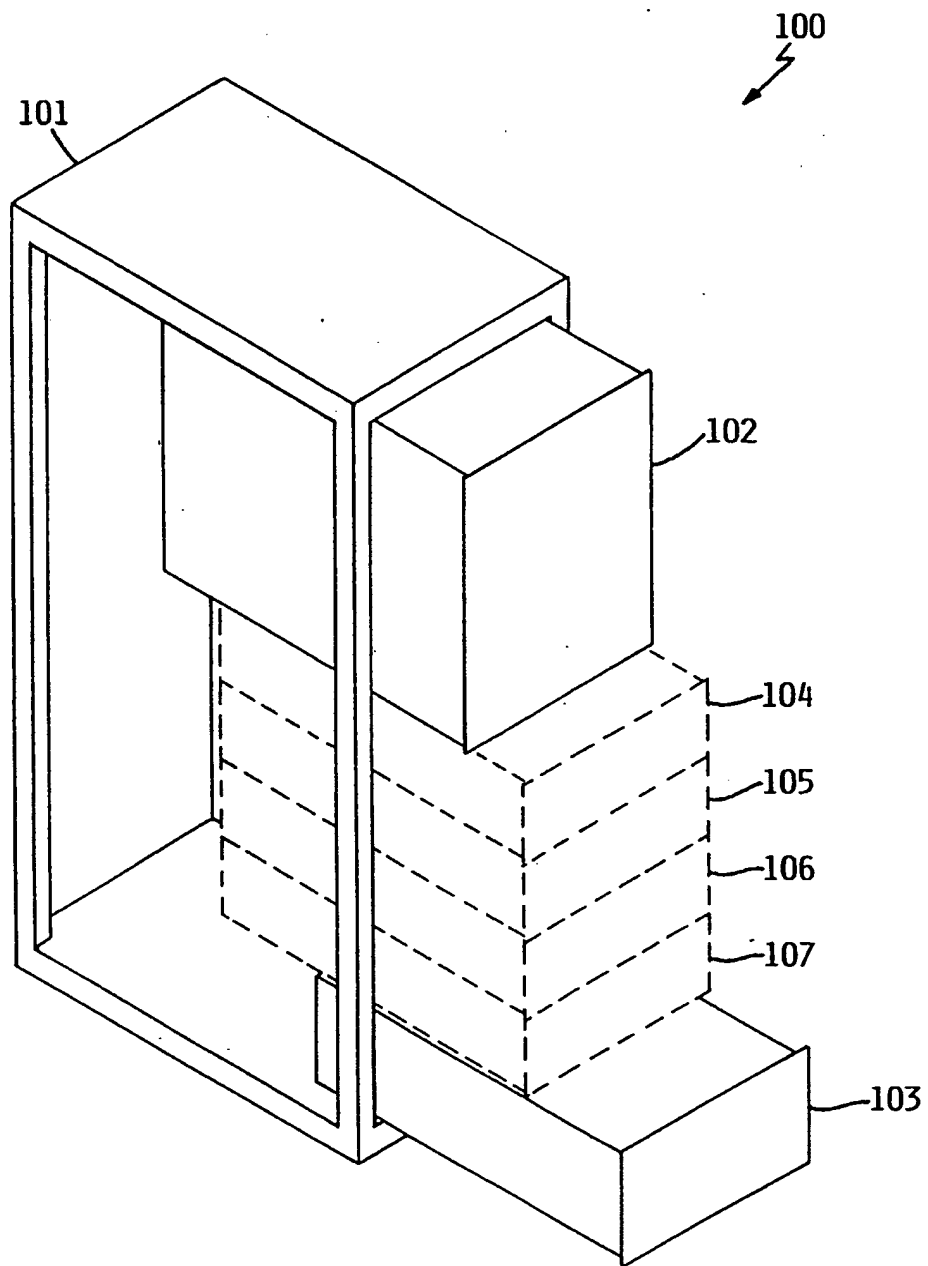


FIG. 1

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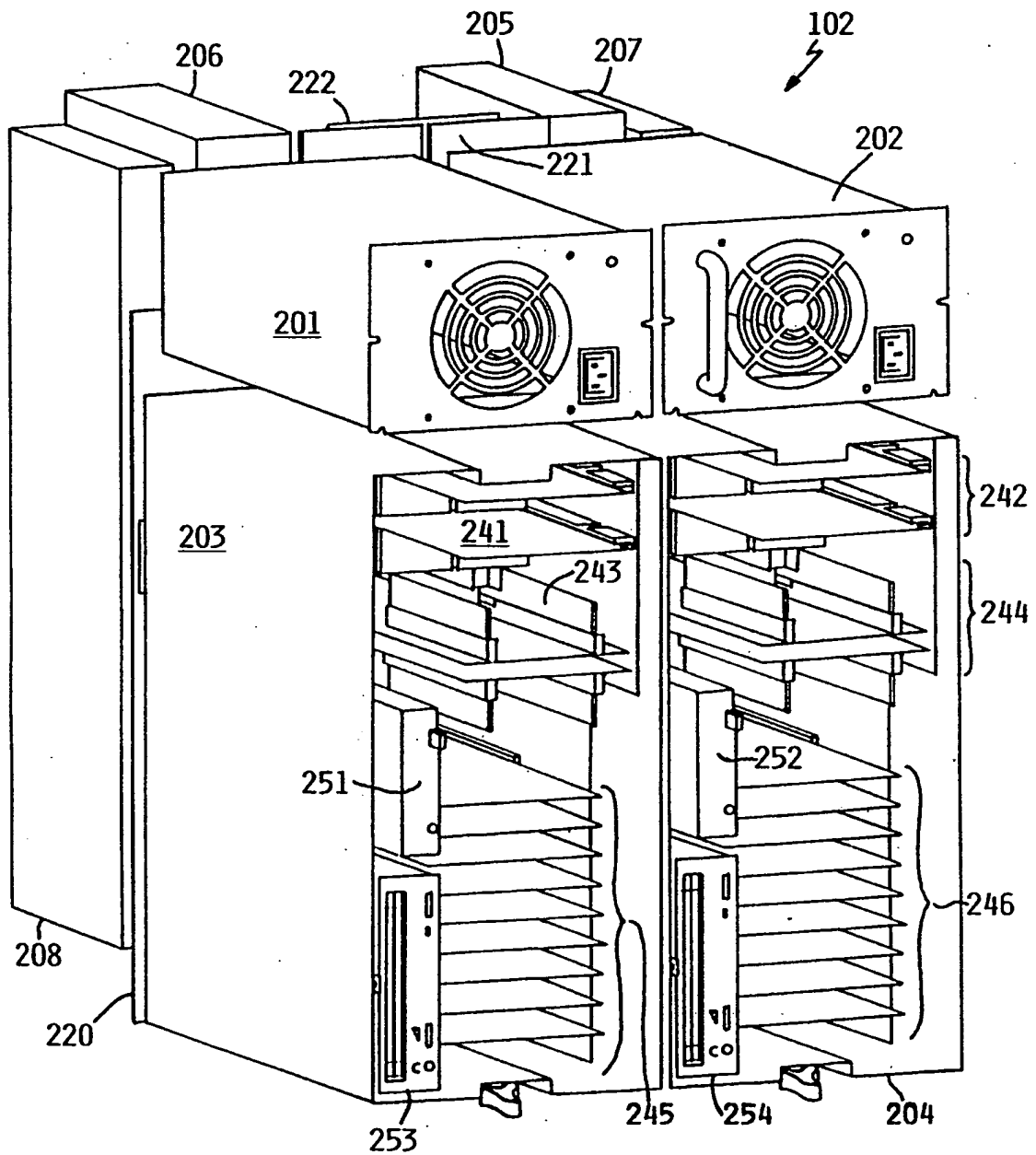


FIG. 2A

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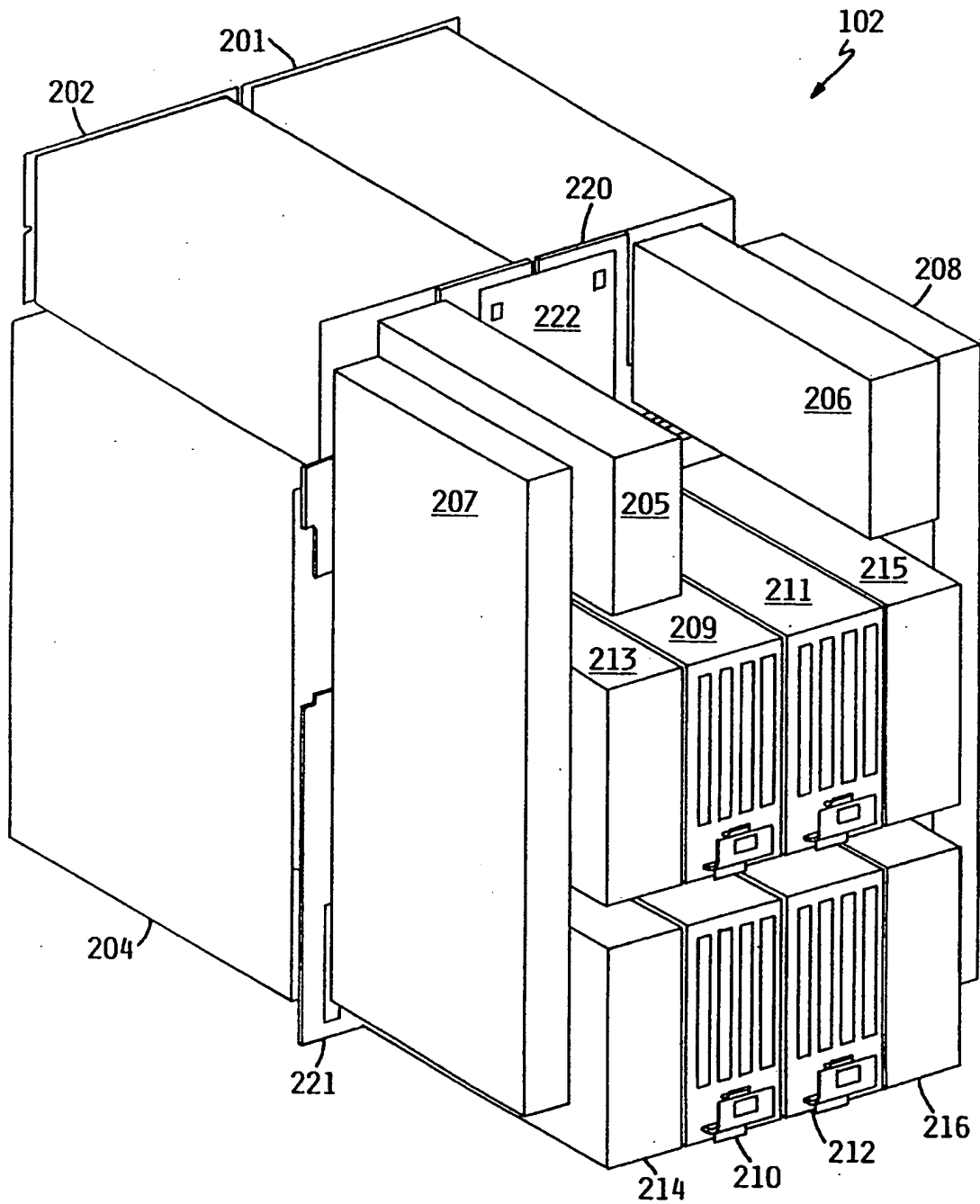


FIG. 2B

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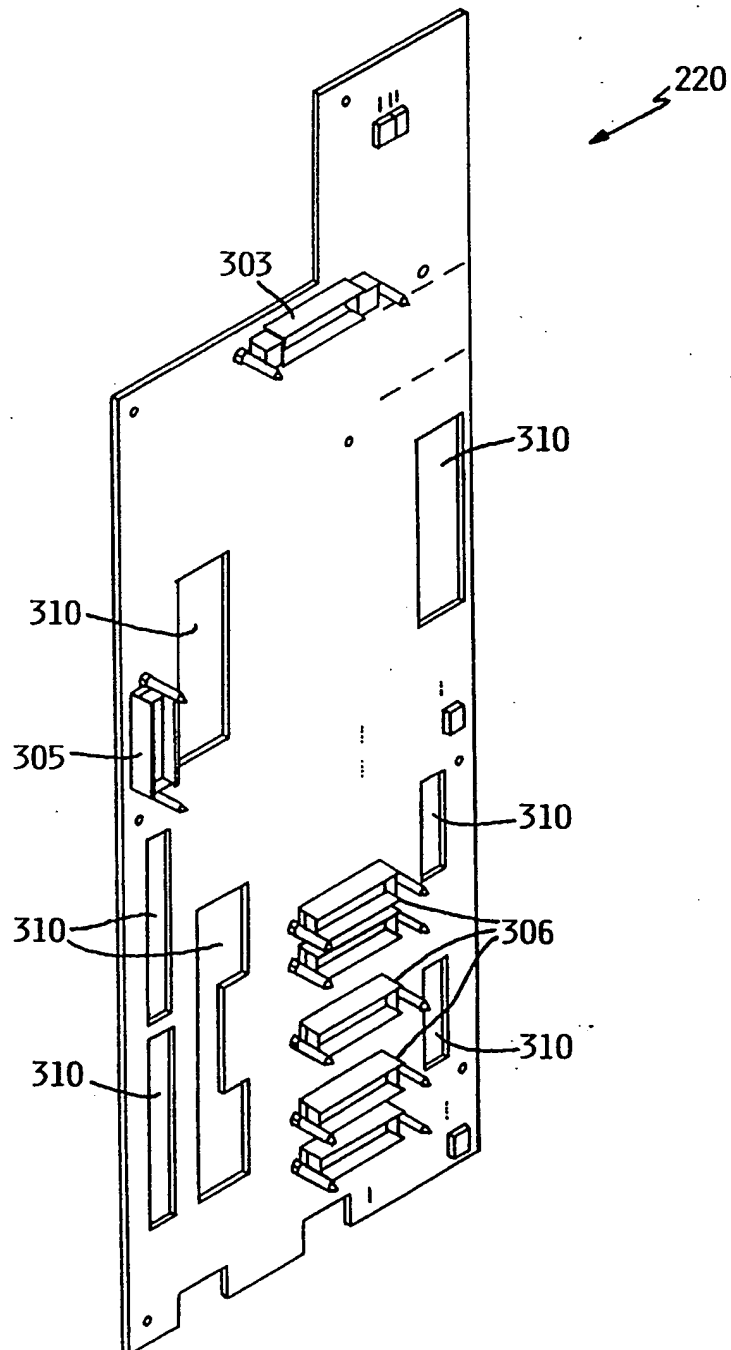


FIG. 3A

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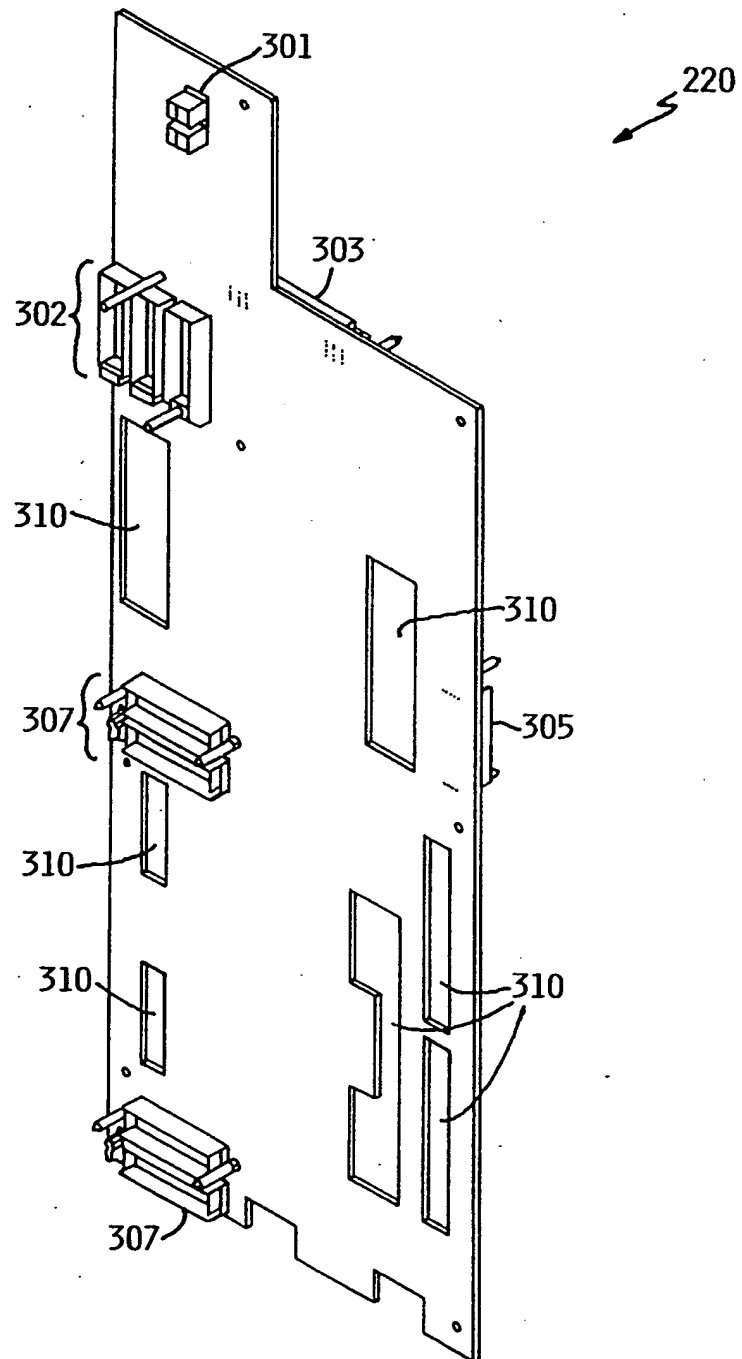


FIG. 3B

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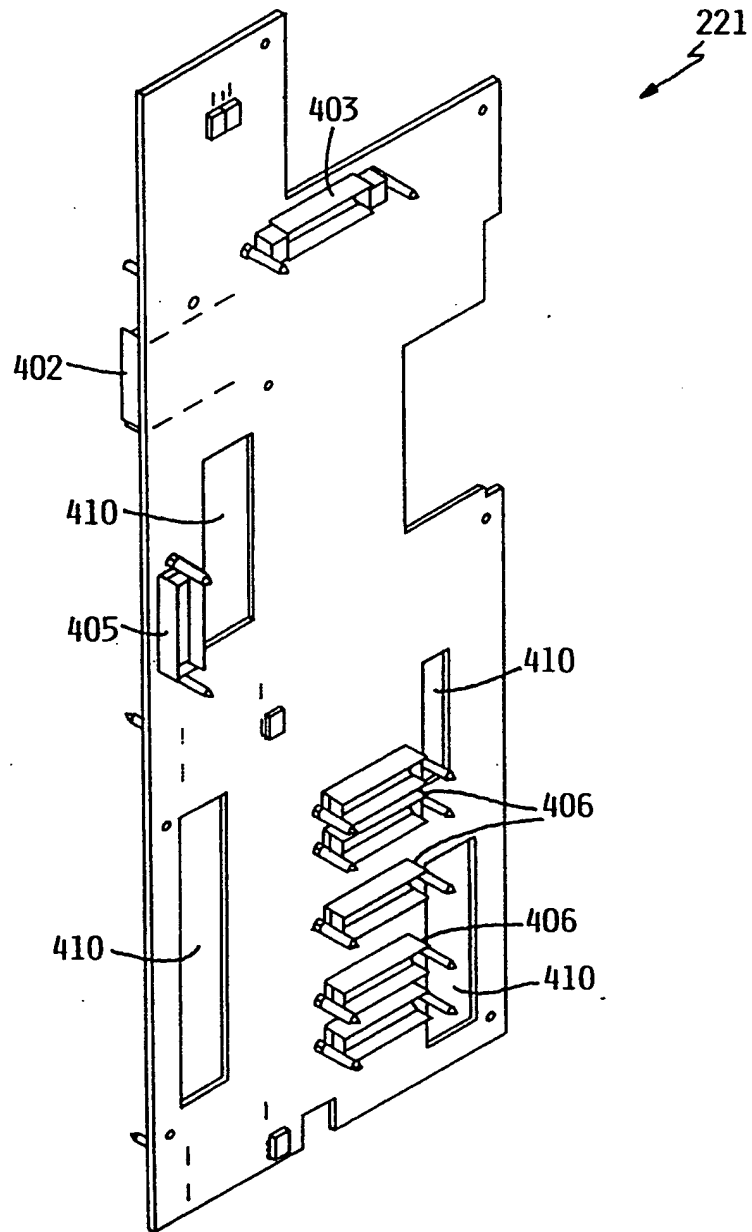


FIG. 4A

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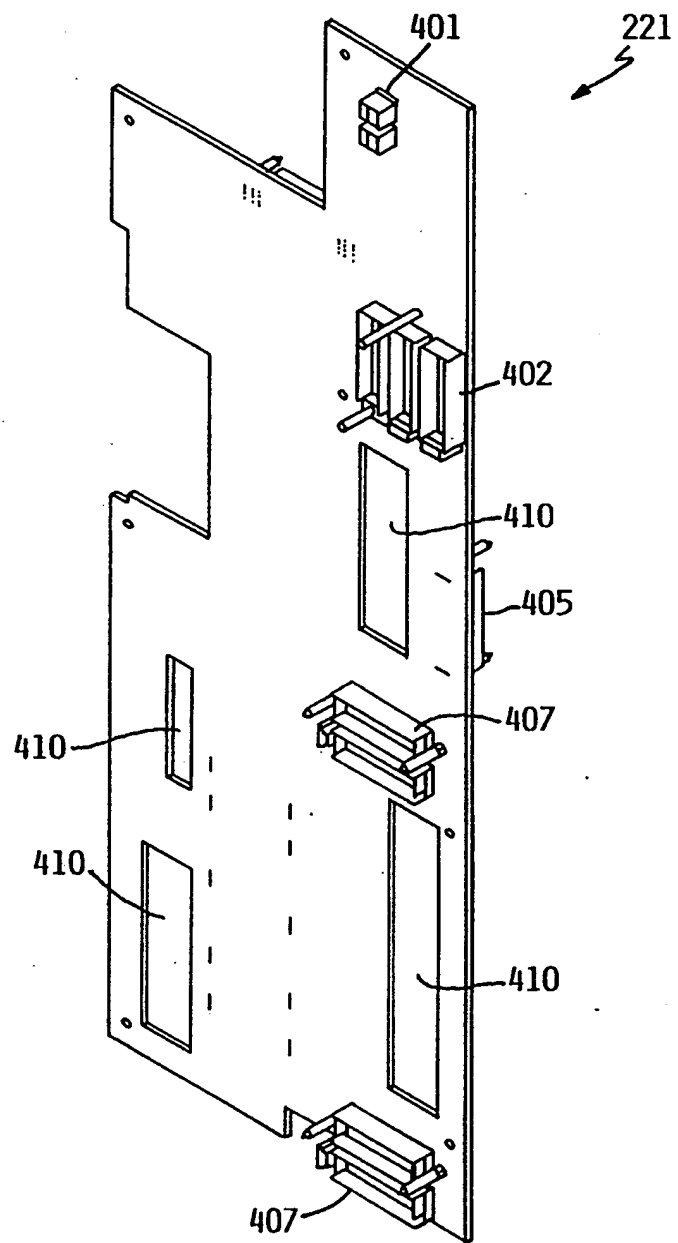


FIG. 4B

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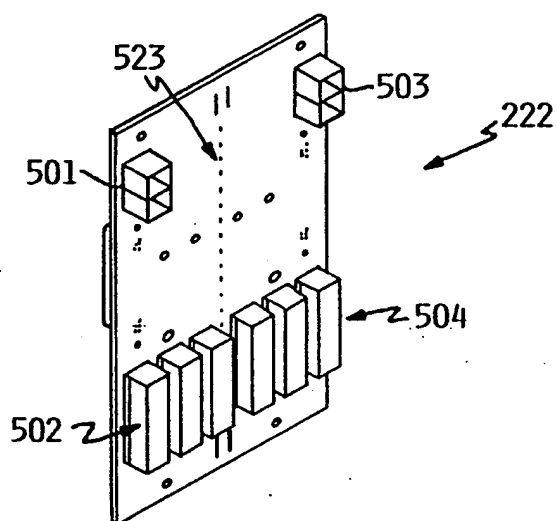


FIG. 5A

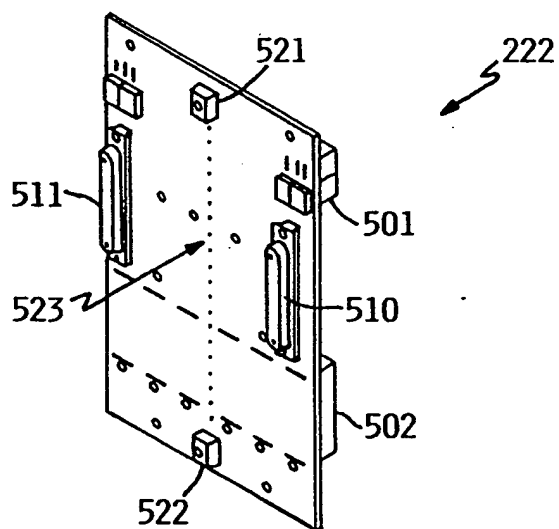


FIG. 5B

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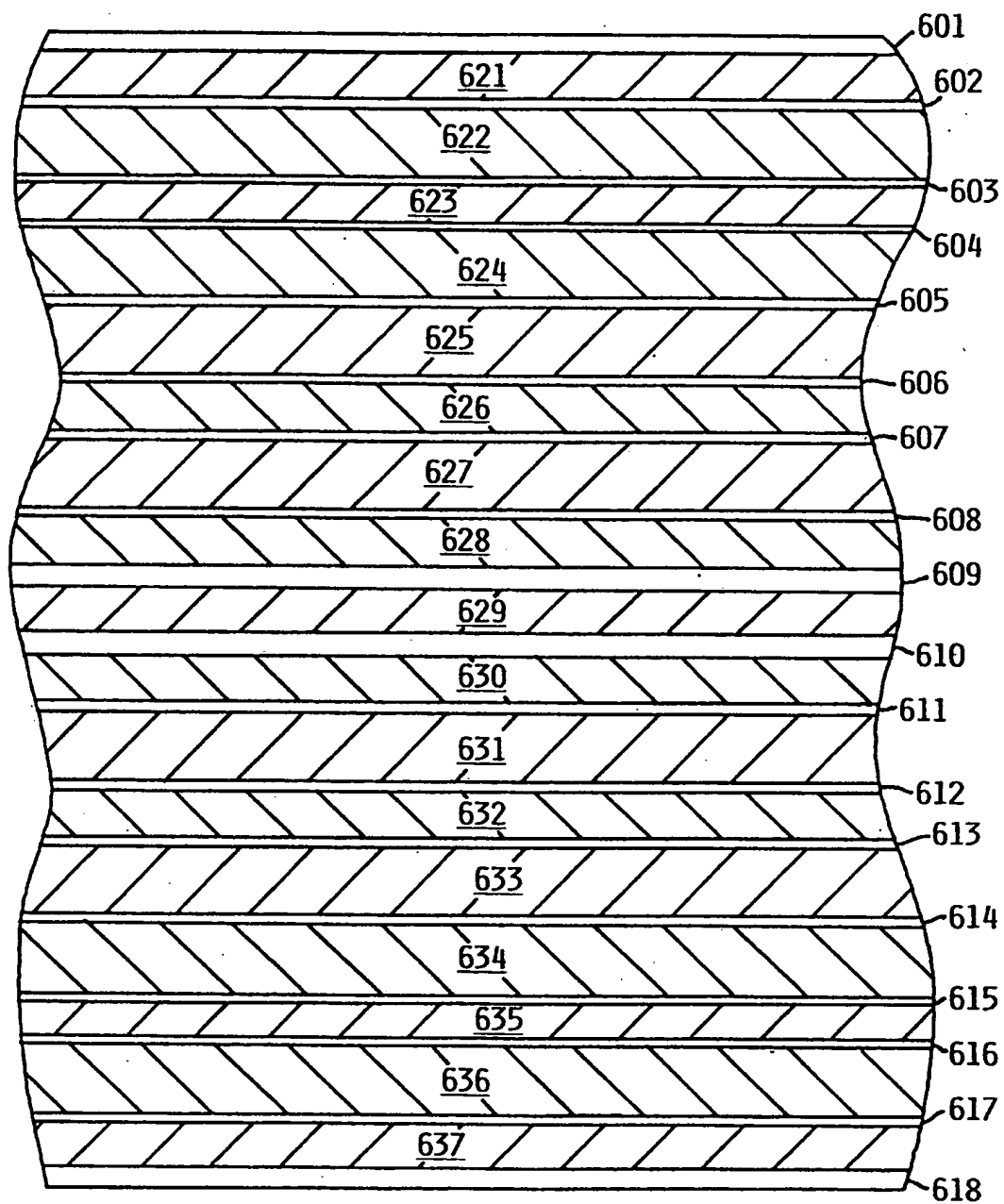


FIG. 6

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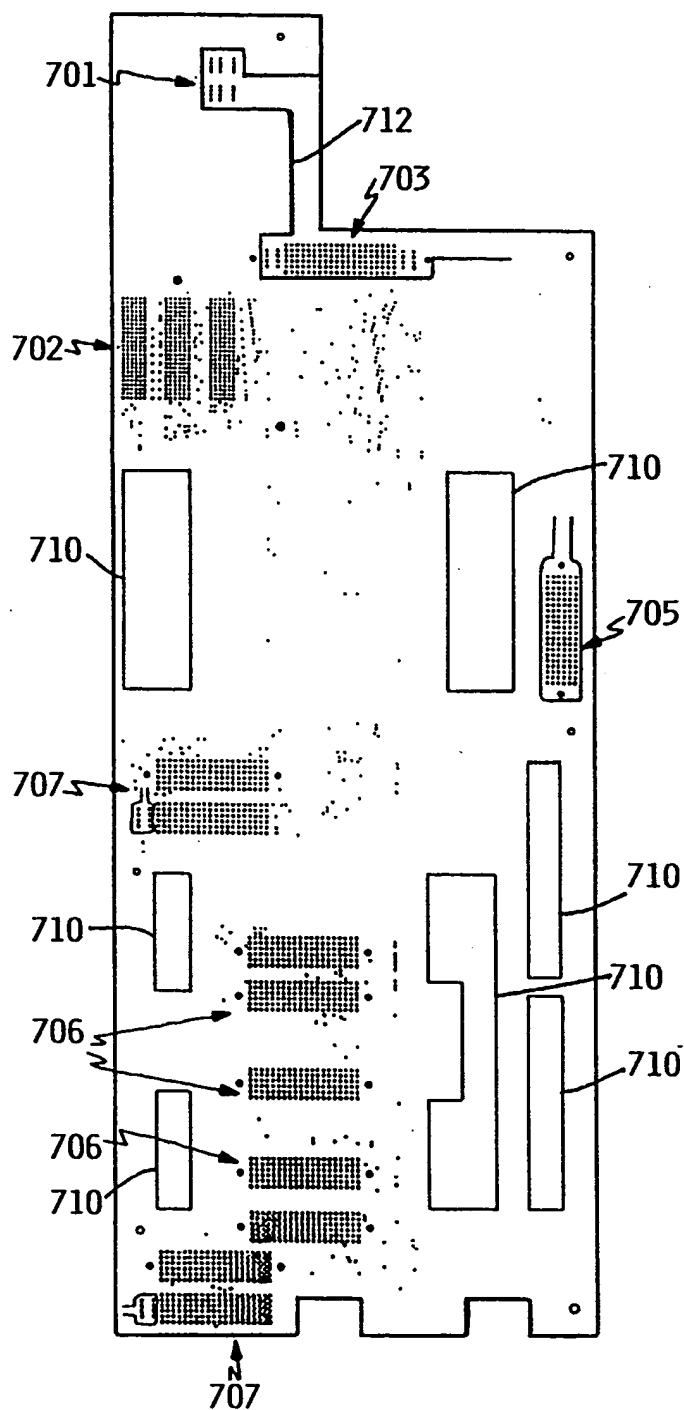


FIG. 7

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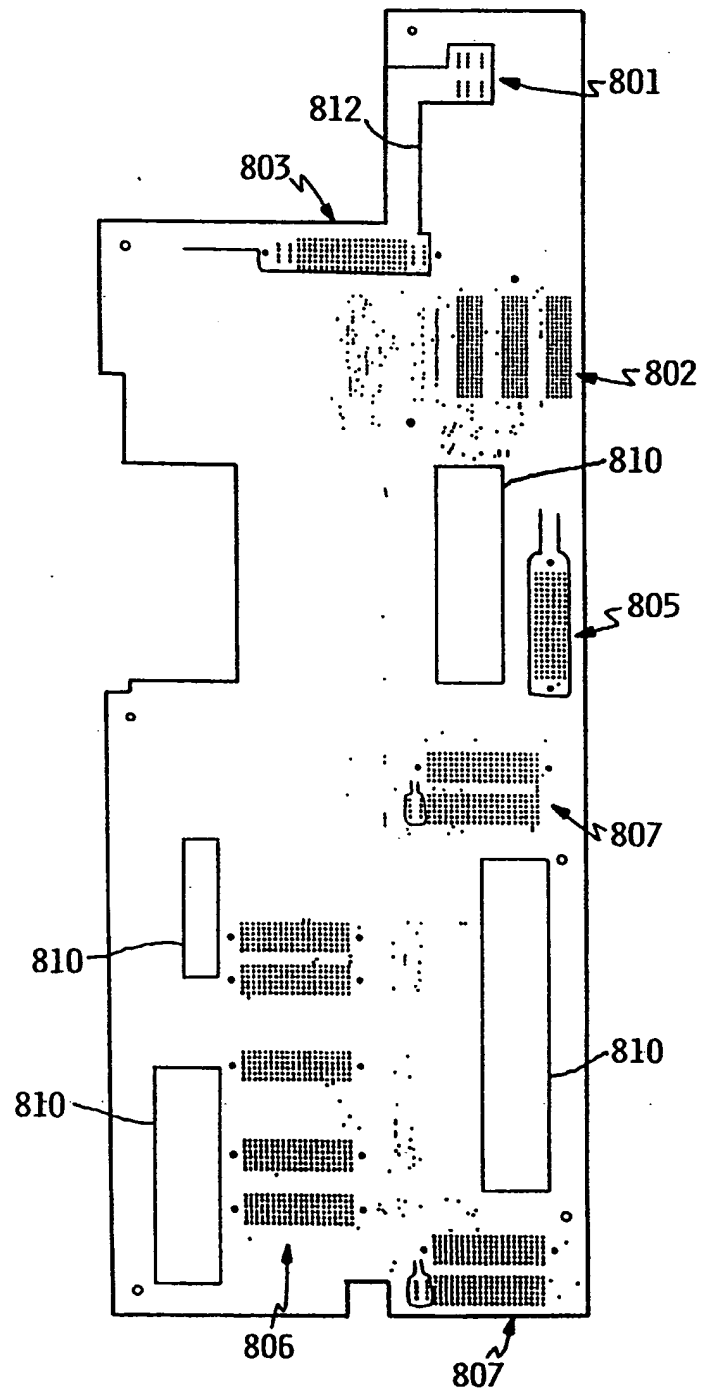


FIG. 8

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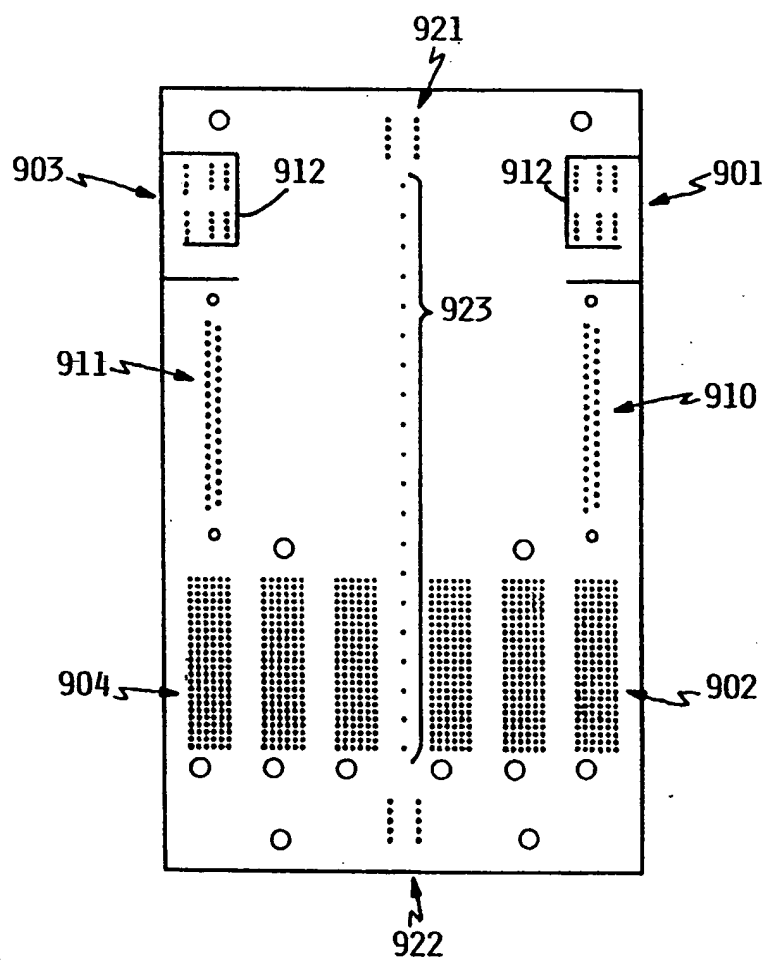


FIG. 9

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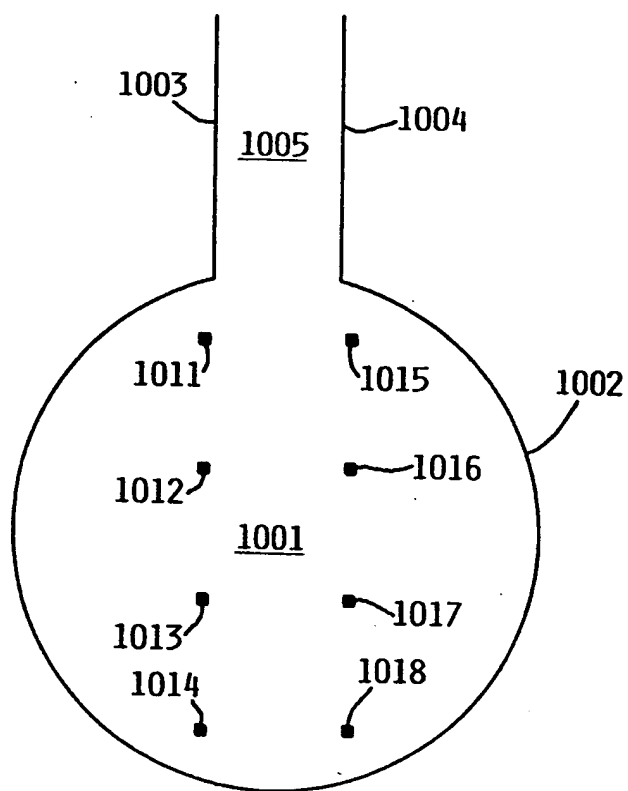


FIG. 10

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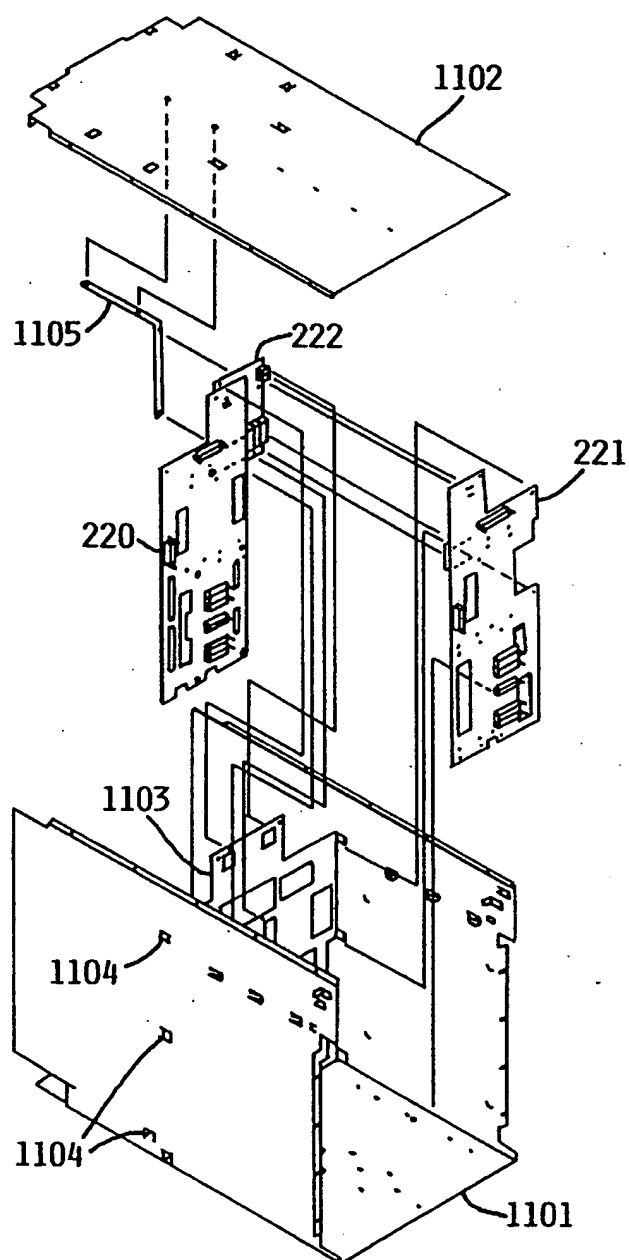


FIG. II

INTERNATIONAL SEARCH REPORT

International application No.
PCT/US99/04873

A. CLASSIFICATION OF SUBJECT MATTER IPC(6) : H05K 5/00, 7/02, 7/10, 7/14; H02J 3/06, 7/00, 7/02, 9/00 US CL : Please See Extra Sheet. According to International Patent Classification (IPC) or to both national classification and IPC																				
B. FIELDS SEARCHED Minimum documentation searched (classification system followed by classification symbols) U.S. : Please See Extra Sheet. Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched Electronic data base consulted during the international search (name of data base and, where practicable, search terms used) IEEE, STN, DERWENT, GPIC, WEST																				
C. DOCUMENTS CONSIDERED TO BE RELEVANT																				
Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.																		
A	US 3,748,500 A (TAM) 24 July 1973 (24/07/73), see entire document, especially columns 6-8.	1-16																		
A	US 3,803,568 A (HIGASHIDE) 09 April 1974 (09/04/74), see entire document, especially figure 1.	1-16																		
A	US 4,609,829 A (MILBY ET AL.) 02 September 1986 (02/09/86), see entire document, especially figure 3.	1-16																		
A	US 4,675,539 A (NICHOL) 23 June 1987 (23/06/87), see entire document, especially figures 1 and 2.	1-16																		
<input checked="" type="checkbox"/> Further documents are listed in the continuation of Box C. <input type="checkbox"/> See patent family annex.																				
<table border="0"> <tr> <td>* Special categories of cited documents:</td> <td>*T</td> <td>later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention</td> </tr> <tr> <td>*A* document defining the general state of the art which is not considered to be of particular relevance</td> <td>*X*</td> <td>document of particular relevance; the claimed invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone</td> </tr> <tr> <td>*E* earlier document published on or after the international filing date</td> <td>*Y*</td> <td>document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art</td> </tr> <tr> <td>*L* document which may throw doubts on priority claim(s) or which is cited to establish the publication date of another citation or other special reason (as specified)</td> <td>*A*</td> <td>document member of the same patent family</td> </tr> <tr> <td>*O* document referring to an oral disclosure, use, exhibition or other means</td> <td></td> <td></td> </tr> <tr> <td>*P* document published prior to the international filing date but later than the priority date claimed</td> <td></td> <td></td> </tr> </table>			* Special categories of cited documents:	*T	later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention	*A* document defining the general state of the art which is not considered to be of particular relevance	*X*	document of particular relevance; the claimed invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone	*E* earlier document published on or after the international filing date	*Y*	document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art	*L* document which may throw doubts on priority claim(s) or which is cited to establish the publication date of another citation or other special reason (as specified)	*A*	document member of the same patent family	*O* document referring to an oral disclosure, use, exhibition or other means			*P* document published prior to the international filing date but later than the priority date claimed		
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Date of the actual completion of the international search 28 JUNE 1999		Date of mailing of the international search report 16 JUL 1999																		
Name and mailing address of the ISA/US Commissioner of Patents and Trademarks Box PCT Washington, D.C. 20231 Facsimile No. (703) 305-3230		Authorized officer DONALD A. SPARKS <i>pm Kelly</i> Telephone No. (703) 308-1756																		

INTERNATIONAL SEARCH REPORT

International application No.
PCT/US99/04873

C (Continuation). DOCUMENTS CONSIDERED TO BE RELEVANT

Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
A	US RE. 33,087 (BRADFORD ET AL.) 10 October 1989 (10/10/89), see entire document, especially figure 1.	1-16
A	US 5,747,889 A (RAYNHAM ET AL.) 05 May 1998 (05/05/98), see entire document, especially figures 1-7B	1-16

INTERNATIONAL SEARCH REPORT

International application No.
PCT/US99/04873

A. CLASSIFICATION OF SUBJECT MATTER: US CL :

307/37, 43, 64, 65, 70, 71, 150; 361/679, 686, 724, 729, 730, 731, 733, 736, 788, 796, 803; 395/283, 750.01; 439/55, 61, 65, 377; 710/103

B. FIELDS SEARCHED

Minimum documentation searched

Classification System: U.S.

307/37, 43, 64, 65, 70, 71, 150; 361/679, 686, 724, 729, 730, 731, 733, 736, 788, 796, 803; 395/283, 750.01; 439/55, 61, 65, 377; 710/103